



Docket No.: 1872.1003CIPC2(formerly 122.1052CIPC2)

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Shigetoshi TOMIO, et al..

Group Art Unit: 2674

Serial No. 09/974,806

Examiner: Duc Q. Dinh

Filed: October 12, 2001

Batch No:

For: FLAT DISPLAY PANEL HAVING INTERNAL POWER SUPPLY CIRCUIT FOR  
REDUCING POWER CONSUMPTION

**SUPPLEMENTAL DECLARATION FOR AMENDMENT PRESENTING  
CLAIMS FOR MATTER DISCLOSED BUT NOT ORIGINALLY CLAIMED**

As a below named inventor in the application for letters patent for an improvement entitled FLAT DISPLAY PANEL HAVING INTERNAL POWER SUPPLY CIRCUIT FOR REDUCING POWER CONSUMPTION, Serial No. 09/974,806, filed in the United States Patent and Trademark Office on October 12, 2001, I hereby declare that the subject matter of the amendment(s) made to the specification, claims and Abstract and/or drawings of the subject application during prosecution by the Amendments filed on or about September 20, 2002; February 19, 2003; March 29, 2004; October 15, 2004 and September 19, 2005 was part of my invention and was invented before the filing of the original application, above identified, for such invention.

**ACKNOWLEDGEMENT OF REVIEW OF PAPERS AND DUTY OF CANDOR**

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, Abstract and drawings each as amended by the Amendments as referred to above.

I acknowledge the duty under 37 CFR 1.56 to disclose information which is material to the examination of this application, namely, information where there is a substantial likelihood that a reasonable examiner would consider it important in deciding whether to allow the application to issue as a patent.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true and, further, that these statements are made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Shigetoshi Tomio  
Shigetoshi TOMIO

15. Feb 2006  
(Date)

Naoki Matsui  
Naoki MATSUI

20. Feb. 2006  
(Date)

Shinpei Yao  
Shinpei YAO

24. Feb. 2006  
(Date)